
LAB REPORT 1

September 18, 2019

Jonathan Gan Bianca Silva
Florida Polytechnic University
Digital Logic Design

Contents

0.1	Objective and Experimental Equipment	2
0.2	Theoretical Analysis	2
0.3	Methods/Procedure	2
0.4	Results and Discussion	2
0.5	Truth Tables, Expressions* and K-Maps*	2
0.6	Multisim	2
0.7	IC Diagrams	2
0.8	Conclusion	2

0.1 OBJECTIVE AND EXPERIMENTAL EQUIPMENT

Insert objective from the lab manual. Insert experimental equipment from the lab manual.

0.2 THEORETICAL ANALYSIS

Insert definitions as well as discuss the theory behind the experiment.

0.3 METHODS/PROCEDURE

Insert methods/procedure from the lab manual.

0.4 RESULTS AND DISCUSSION

Insert information explaining the results from your physical circuits as it relates to the function and logic gates. Insert well titled and organized truth tables from the lab manual with the data you collected while conducting the experiment. Insert every truth table the lab calls for. Insert Logic Diagrams made in Multisim relating to the functions used in the experiment.

0.5 TRUTH TABLES, EXPRESSIONS* AND K-MAPS*

Add Tables here

0.6 MULTISIM

Not used

0.7 IC DIAGRAMS

0.8 CONCLUSION

REFERENCES