NIT Srinagar thesis format in LaTeX

Thesis submitted by

Student Name

202XMECEMEXXX

in partial fulfilment of the requirements

for the award of the degree of

Master of Technology

in

Micro Electronics

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

NATIONAL INSTITUTE OF TECHNOLOGY, SRINAGAR

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Abstract

The abstract should not exceed 500 words or 2 pages. The abstract should explain the domain of the thesis, identify topic area, include the hypothesis or problem statement, provide a general overview of the methodology, and identify research benefits and key findings and/or contributions.

Acknowledgements

I would like to thank my supervisor... I would also like to thank technical and non-technical staff in the department for their cooperation.

I am extremely grateful to my parents for their love, prayers, care and sacrifices for educating and preparing me for the future...

also add who had any part in your project.

Dedication

This is dedicated to my family. You can remove this page if you want. This is not necessary.

Table of Contents

List of Tables

List of Figures

List of Abbreviations

FET   Field Effect Transistor

MOS   Metal Oxide Semiconductor

List of Symbols

α	alpha
β	beta
μ	mobility

Chapter 1

Introduction

All the best with your thesis writing.

1.1 State of the Art

What is State of the Art?

1.1.1 An Overview

An Overview of your topic:

1.1.2 Another subection

you can have as many sections as you want.

1.1.3 Subsection another one

write and do not stop.

1.2 Other Section

continue with the flow.

Chapter 2

Literature

This is Chapter 02! It contains the bulk of theory and concepts required to understand your thesis. This is how you cite [1] and another citation [2].

2.1 A Section

you can have as many sections as you want.

2.1.1 Subsection 1

this creates a sub-section under the section

Chapter 3

Result and Conclusion

This is the final chapter. It contains result and conclusions.

3.1   Results

you can have as many results as you want.

3.2   Conclusion

be precise

3.3   Future Outlook

for the students after you so it is easy to carry the research you did.

Bibliography

[1] L. Chua. Memristor-the missing circuit element. *IEEE Transactions on Circuit Theory*, 18(5):507–519, 1971. `doi:10.1109/TCT.1971.1083337`.

[2] Leon Chua. Memristor, hodgkin–huxley, and edge of chaos. *Nanotechnology*, 24(38):383001, 2013. `doi:10.1088/0957-4484/24/38/383001`.

Appendix A

Implementation

A.1 Software

Software used... Libraries used... Reference the open sourced model, if used.

A.2 Code

Code availability on request or link of the uploaded code or write using code listing check LaTeX format for that.

```
1  import mumpy as np
2
3  % add code here
```